

**TM12P03S**
**P-Channel Enhancement Mosfet**
**General Description**

- Low  $R_{DS(ON)}$
- RoHS and Halogen-Free Compliant

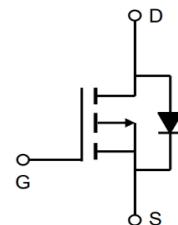
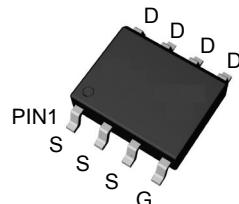
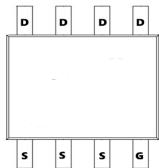
**Applications**

- Load switch
- PWM

**General Features**

$V_{DS} = -30V$   $I_D = -12A$   
 $R_{DS(ON)} = 14.0\text{ m}\Omega$  (typ.) @  $V_{GS} = -10V$

100% UIS Tested  
 100%  $R_g$  Tested


**S:SOP-8L**


Marking: 4407

**Absolute Maximum Ratings** ( $T_A = 25^\circ C$  unless otherwise noted)

Symbol	Parameter	Max.	Units
$V_{DSS}$	Drain-Source Voltage	-30	V
$V_{GSS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current $T_C = 25^\circ C$	-12	A
	$T_C = 100^\circ C$	-8	A
$I_{DM}$	Pulsed Drain Current <sup>note1</sup>	-48	A
$P_D$	Power Dissipation $T_A = 25^\circ C$	3.7	W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	33.8	$^\circ C/W$
$T_J, T_{STG}$	Operating and Storage Temperature Range	-55 to +150	$^\circ C$

**TM12P03S**
**P-Channel Enhancement Mosfet**
**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
<b>Off Characteristic</b>						
$V_{(\text{BR})\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D = -250\mu\text{A}$	-30	-	-	V
$I_{\text{DSS}}$	Zero Gate Voltage Drain Current	$V_{DS} = -30\text{V}$ , $V_{GS}=0\text{V}$ ,	-	-	-1	$\mu\text{A}$
$I_{GSS}$	Gate to Body Leakage Current	$V_{DS}=0\text{V}$ , $V_{GS}= \pm 20\text{V}$	-	-	$\pm 100$	nA
<b>On Characteristics</b>						
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{DS}=V_{GS}$ , $I_D = -250\mu\text{A}$	-1.0	-1.6	-2.5	V
$R_{DS(\text{on})}$ Note3	Static Drain-Source on-Resistance	$V_{GS} = -10\text{V}$ , $I_D = -10\text{A}$	-	14	18	$\text{m}\Omega$
		$V_{GS} = -4.5\text{V}$ , $I_D = -5\text{A}$	-	19	24	
<b>Dynamic Characteristics</b>						
$C_{iss}$	Input Capacitance	$V_{DS} = -15\text{V}$ , $V_{GS}=0\text{V}$ , $f=1.0\text{MHz}$	-	1630	-	pF
$C_{oss}$	Output Capacitance		-	183	-	pF
$C_{rss}$	Reverse Transfer Capacitance		-	156	-	pF
$Q_g$	Total Gate Charge	$V_{DS} = -15\text{V}$ , $I_D = -5\text{A}$ , $V_{GS} = -10\text{V}$	-	22	-	nC
$Q_{gs}$	Gate-Source Charge		-	1.0	-	nC
$Q_{gd}$	Gate-Drain("Miller") Charge		-	1.8	-	nC
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-on Delay Time	$V_{DD} = -15\text{V}$ , $I_D = -10\text{A}$ , $V_{GS} = -10\text{V}$ , $R_{\text{GEN}} = 2.5\Omega$	-	9	-	ns
$t_r$	Turn-on Rise Time		-	13	-	ns
$t_{d(off)}$	Turn-off Delay Time		-	48	-	ns
$t_f$	Turn-off Fall Time		-	20	-	ns
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
$I_s$	Maximum Continuous Drain to Source Diode Forward Current	-	-	-12	-	A
$I_{sM}$	Maximum Pulsed Drain to Source Diode Forward Current	-	-	-40	-	A
$V_{SD}$	Drain to Source Diode Forward Voltage	$V_{GS}=0\text{V}$ , $I_s = -15\text{A}$	-	-0.8	-1.2	V
$trr$	Reverse Recovery Time	$T_J=25^\circ\text{C}$ , $V_{DD} = -24\text{V}$ , $I_F = -2.8\text{A}$ , $dI/dt = -100\text{A}/\mu\text{s}$	-	64	-	ns
$Qrr$	Reverse Recovery Charge		-	25	-	nC

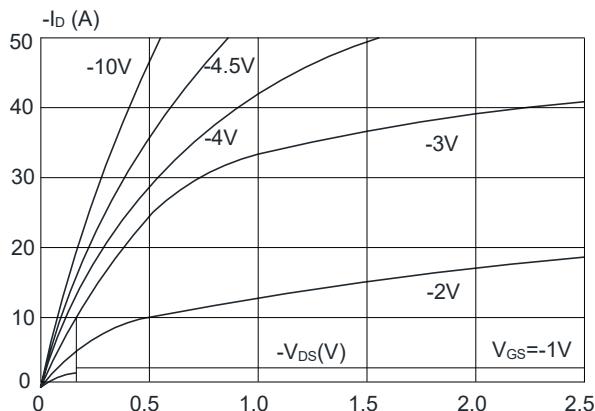
Notes:1. Repetitive Rating: Pulse Width Limited by Maximum Junction Temperature

2. EAS condition:  $T_J=25^\circ\text{C}$ ,  $V_{GS}=10\text{V}$ ,  $R_G=25\Omega$ ,  $L=0.5\text{mH}$ ,  $I_{AS}=-12.7\text{A}$

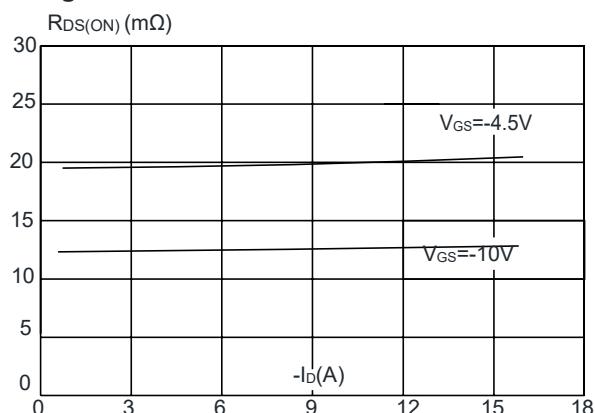
3. Pulse Test: Pulse Width $\leq 300\mu\text{s}$ , Duty Cycle $\leq 0.5\%$

## Typical Performance Characteristics

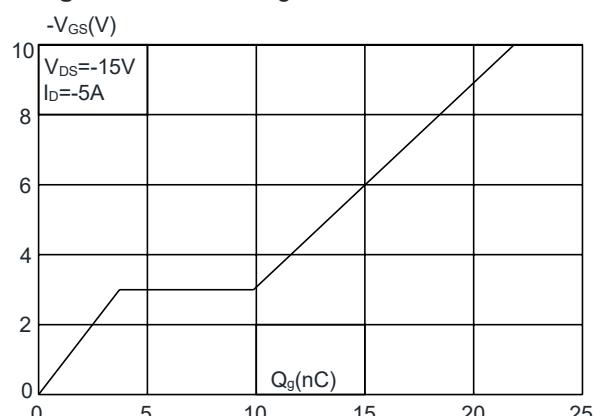
**Figure 1:** Output Characteristics



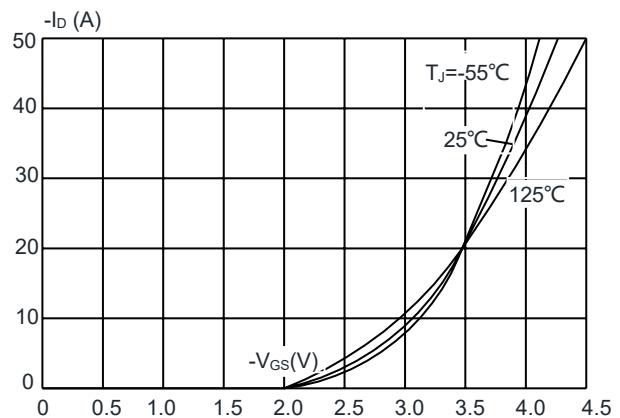
**Figure 3:** On-resistance vs. Drain Current



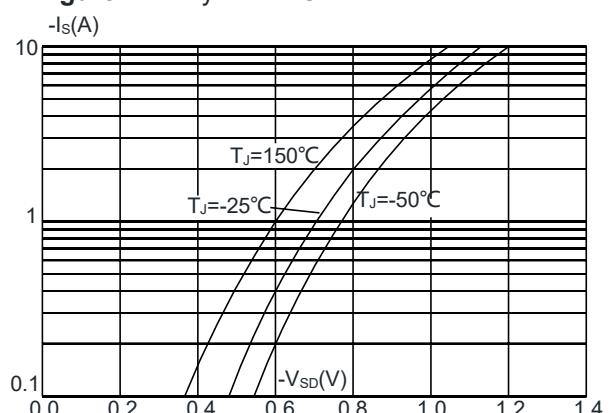
**Figure 5:** Gate Charge Characteristics



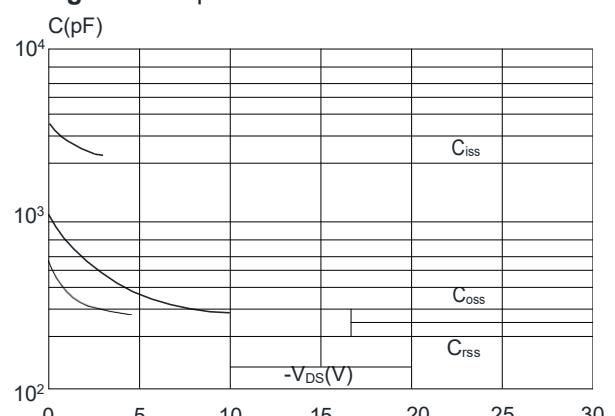
**Figure 2:** Typical Transfer Characteristics



**Figure 4:** Body Diode Characteristics



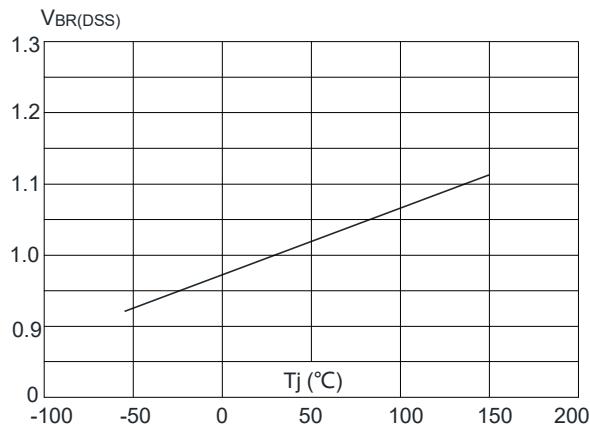
**Figure 6:** Capacitance Characteristics



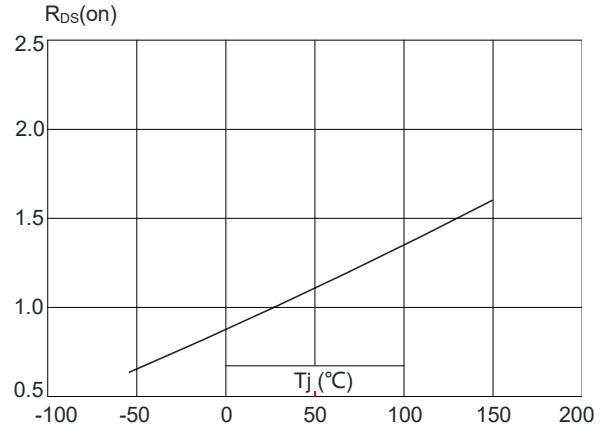
## TM12P03S

## P-Channel Enhancement Mosfet

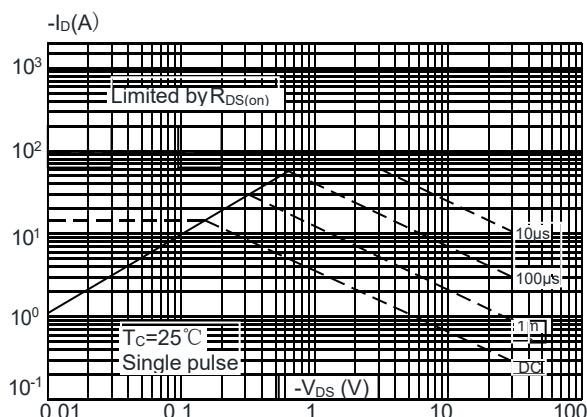
**Figure 7:** Normalized Breakdown Voltage vs. Junction Temperature



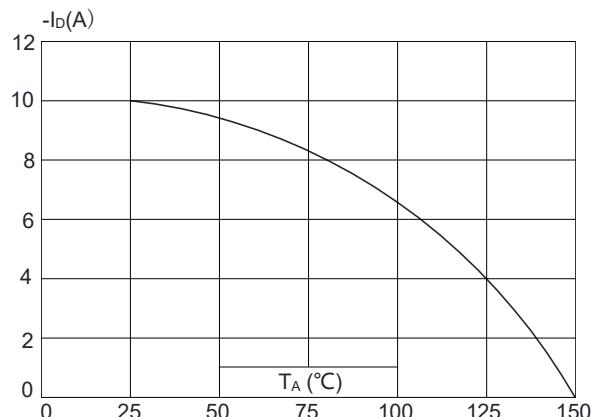
**Figure 8:** Normalized on Resistance vs. Junction Temperature



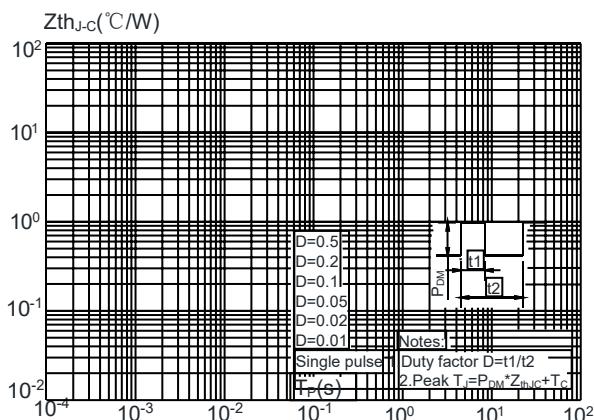
**Figure 9:** Maximum Safe Operating Area



**Figure 10:** Maximum Continuous Drain Current vs. Ambient Temperature



**Figure 11:** Maximum Effective Transient Thermal Impedance, Junction-to-Case

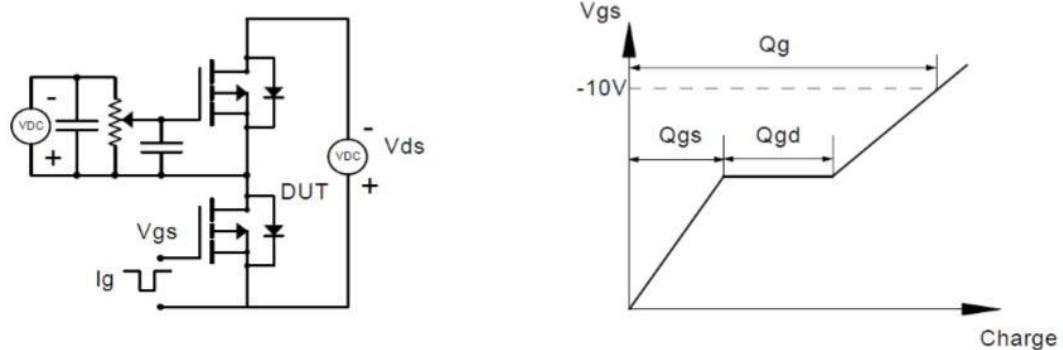


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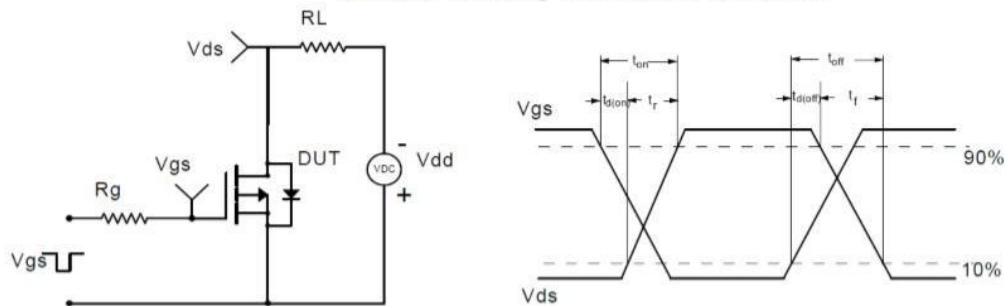
## P-Channel Enhancement Mosfet

### Test Circuit

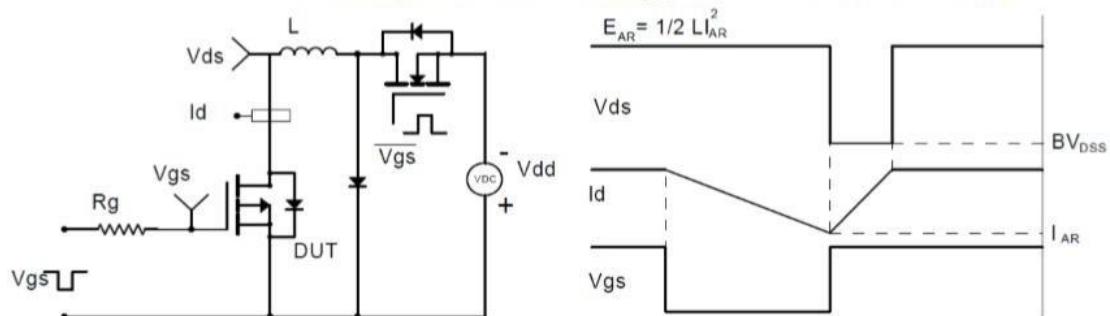
Gate Charge Test Circuit & Waveform



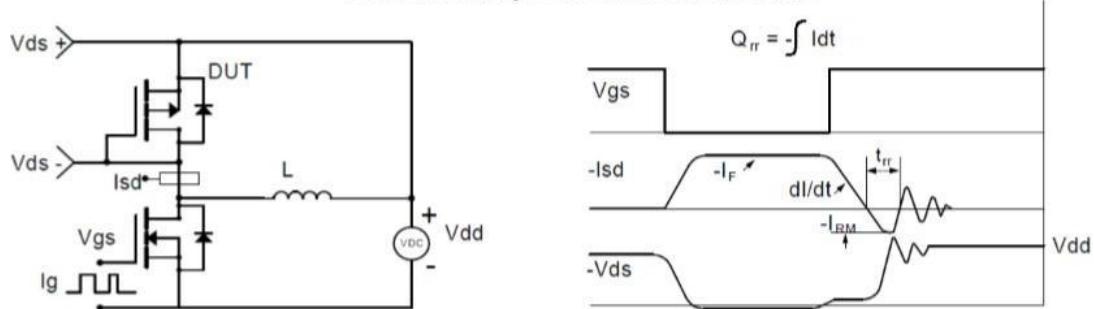
Resistive Switching Test Circuit & Waveforms



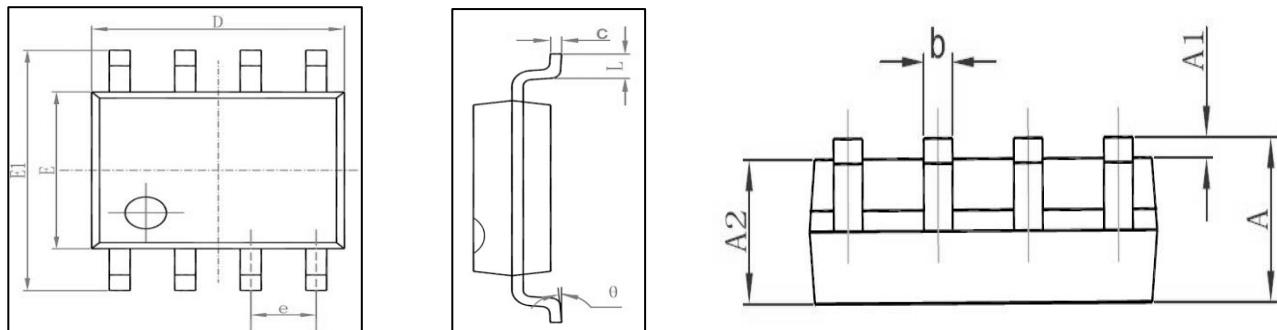
Unclamped Inductive Switching (UIS) Test Circuit & Waveforms



Diode Recovery Test Circuit & Waveforms



## Package Mechanical Data:SOP-8L



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°

